**Preferred Device** 

# Power MOSFET 2.0 Amps, 60 Volts

# N-Channel SOT-223

Designed for low voltage, high speed switching applications in power supplies, converters and power motor controls and bridge circuits.

#### **Applications**

- Power Supplies
- Converters
- Power Motor Controls
- Bridge Circuits

#### **MAXIMUM RATINGS** (T<sub>C</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	VDSS	60	Vdc
Drain-to-Gate Voltage (RGS = 1.0 M $\Omega$ )	V <sub>DGR</sub>	60	Vdc
Gate–to–Source Voltage  – Continuous  – Non–repetitive (t <sub>p</sub> ≤ 10 ms)	V <sub>GS</sub>	± 20 ± 30	Vdc Vpk
Drain Current  - Continuous @ $T_A = 25^{\circ}C$ - Continuous @ $T_A = 100^{\circ}C$ - Single Pulse ( $t_p \le 10 \ \mu s$ )	I <sub>D</sub> I <sub>D</sub>	2.0 1.2 6.0	Adc Apk
Total Power Dissipation @ T <sub>A</sub> = 25°C (Note 1.) Total Power Dissipation @ T <sub>A</sub> = 25°C (Note 2.) Derate above 25°C	PD	2.1 1.3 0.014	W W W/°C
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to 175	ů
Single Pulse Drain-to-Source Avalanche Energy – Starting T <sub>J</sub> = 25°C (V <sub>DD</sub> = 25 Vdc, V <sub>GS</sub> = 10 Vdc, I <sub>L</sub> (pk) = 6.0 Apk, L = 10 mH, V <sub>DS</sub> = 60 Vdc)	E <sub>AS</sub>	65	mJ
Thermal Resistance  – Junction to Ambient (Note 1.)  – Junction to Ambient (Note 2.)	R <sub>θ</sub> JA R <sub>θ</sub> JA	72.3 114	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C

- When surface mounted to an FR4 board using 1" pad size, (Cu. Area 1.127 in<sup>2</sup>).
- When surface mounted to an FR4 board using minimum recommended pad size, 2–2.4 oz. (Cu. Area 0.272 in<sup>2</sup>).

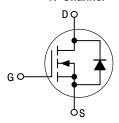


### ON Semiconductor™

http://onsemi.com

# 2.0 AMPERES 60 VOLTS RDS(on) = 160 m $\Omega$

#### N-Channel



#### MARKING DIAGRAM



SOT-223 CASE 318E STYLE 3



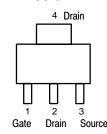
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WW

Device CodeLocation Code

= Work Week

#### **PIN ASSIGNMENT**



#### **ORDERING INFORMATION**

Device	Package	Shipping
NTF3055-160T1	SOT-223	1000 Tape & Reel
NTF3055-160T3	SOT-223	4000 Tape & Reel
NTF3055-160T3LF	SOT-223	4000 Tape & Reel

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Charac	teristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage (V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = 250 μAdc) Temperature Coefficient (Positive)	(Note 3.)	V(BR)DSS	60 -	72 72	_ _	Vdc mV/°C
Zero Gate Voltage Drain Current (VDS = 60 Vdc, VGS = 0 Vdc) (VDS = 60 Vdc, VGS = 0 Vdc, TJ =	= 150°C)	IDSS	_ _	- -	1.0 10	μAdc
Gate-Body Leakage Current (VG	$S = \pm 20 \text{ Vdc}, V_{DS} = 0 \text{ Vdc}$	IGSS	-	-	± 100	nAdc
ON CHARACTERISTICS (Note 3.)						
Gate Threshold Voltage (Note 3.) (VDS = VGS, ID = 250 μAdc) Threshold Temperature Coefficient (N	legative)	VGS(th)	2.0	3.1 6.6	4.0 -	Vdc mV/°C
Static Drain-to-Source On-Resistance (Note 3.) (VGS = 10 Vdc, I <sub>D</sub> = 1.0 Adc)		R <sub>DS(on)</sub>	ı	142	160	mΩ
Static Drain-to-Source On-Resistan (VGS = 10 Vdc, I <sub>D</sub> = 2.0 Adc) (VGS = 10 Vdc, I <sub>D</sub> = 1.0 Adc, T <sub>J</sub> =		VDS(on)	_	0.142 0.270	0.384 -	Vdc
Forward Transconductance (Note 3.)	$(V_{DS} = 8.0 \text{ Vdc}, I_{D} = 1.5 \text{ Adc})$	9fs	-	1.8	_	Mhos
DYNAMIC CHARACTERISTICS						
Input Capacitance		C <sub>iss</sub>	-	200	280	pF
Output Capacitance	$(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0 \text{ V}, $ f = 1.0 MHz)	C <sub>oss</sub>	-	68	100	
Transfer Capacitance	· ·····	C <sub>rss</sub>	-	26	40	
SWITCHING CHARACTERISTICS	6 (Note 4.)					
Turn-On Delay Time		<sup>t</sup> d(on)	-	9.2	20	ns
Rise Time	$(V_{DD} = 30 \text{ Vdc}, I_{D} = 2.0 \text{ Adc},$	t <sub>r</sub>	1	9.2	20	-
Turn-Off Delay Time	$V_{GS} = 10 \text{ Vdc},$ $R_G = 9.1 \Omega) \text{ (Note 3.)}$	td(off)	-	16	40	
Fall Time		tf	-	9.2	20	
Gate Charge		QT	-	6.9	14	nC
	(V <sub>DS</sub> = 48 Vdc, I <sub>D</sub> = 2.0 Adc, V <sub>GS</sub> = 10 Vdc) (Note 3.)	Q <sub>1</sub>	_	1.4	-	
		Q <sub>2</sub>	1	3.0	_	
SOURCE-DRAIN DIODE CHARA	CTERISTICS					
Forward On–Voltage	$(I_S = 2.0 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ $(I_S = 2.0 \text{ Adc}, V_{GS} = 0 \text{ Vdc},$ $T_J = 150^{\circ}\text{C}) \text{ (Note 3.)}$	V <sub>SD</sub>	_ _	0.86 0.70	1.0	Vdc
Reverse Recovery Time		t <sub>rr</sub>	_	28.9	_	ns
	(I <sub>S</sub> = 2.0 Adc, V <sub>GS</sub> = 0 Vdc,	ta	-	19.1	_	1
	$dI_S/dt = 100 A/\mu s)$ (Note 3.)	t <sub>b</sub>	_	9.8	_	1
Reverse Recovery Stored Charge		Q <sub>RR</sub>	-	0.030	-	μС

<sup>3.</sup> Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

<sup>4.</sup> Switching characteristics are independent of operating junction temperatures.

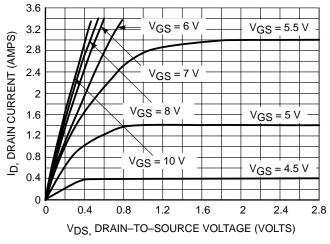


Figure 1. On-Region Characteristics

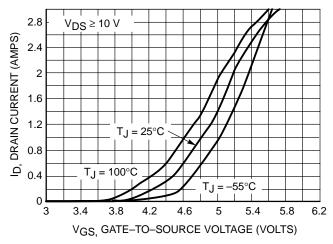


Figure 2. Transfer Characteristics

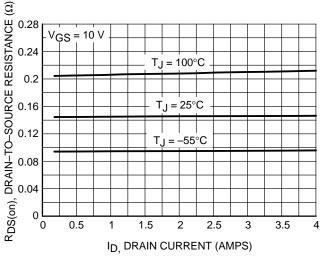


Figure 3. On-Resistance versus Gate-to-Source Voltage

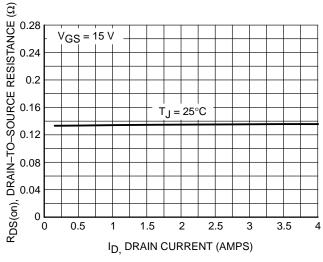


Figure 4. On-Resistance versus Drain Current and Gate Voltage

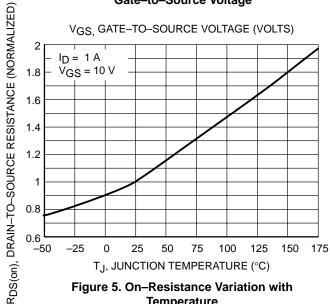


Figure 5. On-Resistance Variation with **Temperature** 

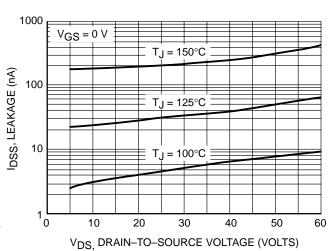


Figure 6. Drain-to-Source Leakage Current versus Voltage

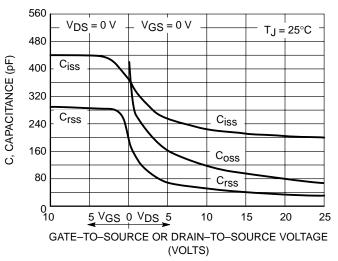


Figure 7. Capacitance Variation

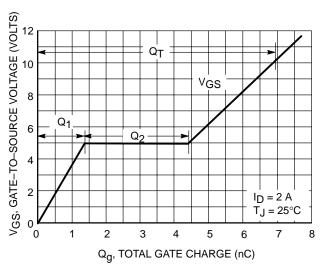


Figure 8. Gate-to-Source and Drain-to-Source Voltage versus Total Charge

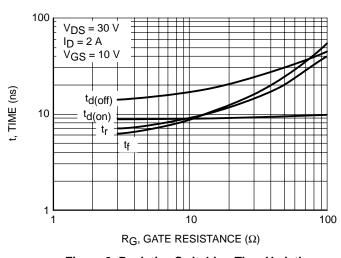


Figure 9. Resistive Switching Time Variation versus Gate Resistance

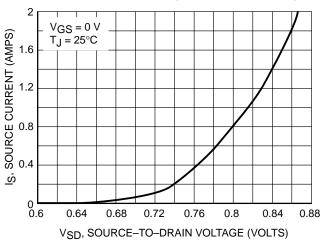


Figure 10. Diode Forward Voltage versus Current

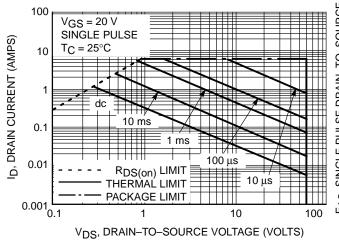


Figure 11. Maximum Rated Forward Biased Safe Operating Area

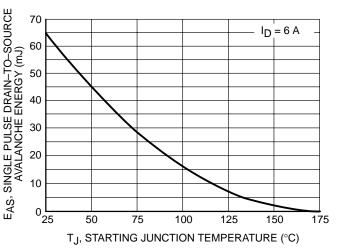


Figure 12. Maximum Avalanche Energy versus Starting Junction Temperature

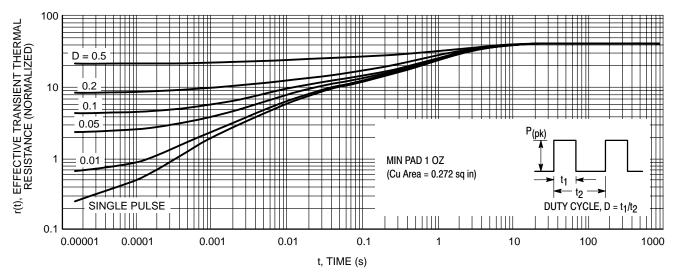
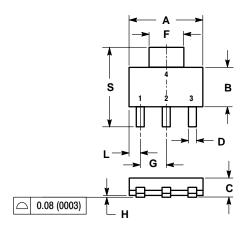
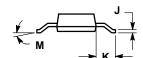


Figure 13. Thermal Response

#### **PACKAGE DIMENSIONS**

**SOT-223 (TO-261)** CASE 318E-04 ISSUE K





#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.249	0.263	6.30	6.70	
В	0.130	0.145	3.30	3.70	
С	0.060	0.068	1.50	1.75	
D	0.024	0.035	0.60	0.89	
F	0.115	0.126	2.90	3.20	
G	0.087	0.094	2.20	2.40	
Н	0.0008	0.0040	0.020	0.100	
J	0.009	0.014	0.24	0.35	
K	0.060	0.078	1.50	2.00	
L	0.033	0.041	0.85	1.05	
M	0 °	10 °	0 °	10 °	
S	0.264	0.287	6.70	7.30	

- STYLE 3: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN

# **Notes**

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